

# Spirent mX2

## 40GbE and 10GbE Dual-Speed Test Modules

The Spirent mX2 40G/10G and 10G/1G Ethernet test modules support the highest performing and most realistic Layer 2-7 control and user plane capabilities for validating systems at their limits. A single module is capable of generating and analyzing line rate stateful and stateless traffic from all ports simultaneously with high-scale routing, access, mobile and enterprise application traffic. With up to 36 40GE ports or 144 10GE ports in a single Spirent chassis, the mX2 is the highest density test module in the industry in its class and scales to 1.44 Tbps of stateful data performance, 9 million mobile subscribers, and 1.6 million BGP sessions.

The Spirent mX2 40GbE/10GbE and 10GbE/1GbE test module architecture combines Spirent Cloud Core™ with high performing multi-core CPUs to intelligently distribute processing resources across ports. This enables superior scale testing involving multiple protocols running simultaneously on the same port—perfect for testing converged devices such as PE routers and mobile gateways. By combining Cloud Core processing and the deep real-time analysis that Spirent TestCenter is known for, the mX2 delivers enhanced realism with scale and performance.

The Spirent mX2 module is available in several port count and speed variations to match your test needs and budget. For dual speed 40GbE versions each of the 40G ports can be configured as 4x10GbE for a maximum density of twelve 10GbE ports per slot. Dual speed modules are also available for 10GbE/1GbE operation from a single port.

### Applications

- **High Density Core Routers**—Tests the throughput, control plane scale, and route capacity of next-gen high density core routers
- **Mobile Gateways**—Validates IP throughput and Any G mobility with millions of subscribers per port, line-rate data with minimum sized packets and detailed per mobile statistics
- **Cloud Infrastructure & Applications**—Ensures security devices, IDS/IPS, load balancers and applications meet their performance, availability, security and scale requirements
- **High Scale Edge & Aggregation Routers**—Test convergence, reliability and scalability of complex, multi-protocol topologies with unprecedented scale and realism

### Features & benefits

#### Performance and flexibility

- Line rate traffic with realism for stress-testing the most complex converged IP systems such as service provider MPLS networks, cloud-scale data centers, and 4G/LTE mobile networks
- Spirent Cloud Core CPU and FPGA-based Layer 2-3 architecture are combined to provide the highest density Layer 2-7 architectures test module in its class
- Multi-speed modules can be software switched to run at 40GE/4 x 10GE and 10GE/1GE
- Available test packages with integrated configuration wizards simplify and accelerate applicable test packages with integrated wizards simplify configuration of ultra-high scale mobility, mobile backhaul, routing, access and application test cases



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## Technical Specifications

### Spirent mX2 Module

Maximum Port Density	Speed	Maximum Ports per Slot	Maximum Ports per STP-N11U Chassis	Maximum Ports Per SPT-N4U Chassis
mX2-40G-Q3 supports dual-speed 40/10GbE mX2-40GO-Q3 supports 40GbE only	40GbE	3	36	6
mX2-10GbE-Q3 supports 10GbE only	10GbE	12	144	24
mX2-10GbE-S12 supports dual speed 10/1GbE	10/1GbE	12	144	24
Media Support (See accessory table below for part numbers)	<ul style="list-style-type: none"> <li>■ 40GBASE-CR4 (with Clause 73 Auto-Negotiation and Link Training)</li> <li>■ 40GBASE-SR4</li> <li>■ 40GBASE-LR4</li> </ul>		<ul style="list-style-type: none"> <li>■ 10GE Direct Attach Copper Cable</li> <li>■ 10GBASE-SR</li> <li>■ 10GBASE-LR</li> <li>■ 1000BASE-T (SFP+ interface modules only)</li> </ul>	
Line clocking and packet time stamping – MX2 modules get their transmit line clocking and time-stamping from the control modules on the SPT-N11U and SPT-N4U.	<ul style="list-style-type: none"> <li>■ Stratum-3 rated oscillator is the default time source. Transmit line clock is at the precise nominal Ethernet rate <math>\pm 1</math> PPM on initial shipment. Accurate to <math>\pm 4.6</math> PPM over 15 years of operation.</li> <li>■ Frame time stamp resolution of 2.5ns.</li> <li>■ GPS and CDMA-based external time sources are supported</li> <li>■ IEEE 1588v2 and NTP packet-based external time sources are supported</li> <li>■ TIA/EIA-95B-based external time sources are supported</li> </ul>			
Inter-module and Inter-chassis Time Synchronization	<p>Modules in the same chassis are phase-locked to the timing source of the control module. For modules in separate chassis:</p> <ul style="list-style-type: none"> <li>■ Spirent-patented self-calibrating inter-chassis timing chain using dedicated port on chassis control module delivers precise synchronization +/- 20ns</li> <li>■ Synchronized via external GPS or CDMA network</li> <li>■ Using IEEE 1588 or NTP packet-based approaches</li> <li>■ With TIA/EIA-95B timing inputs</li> </ul>			
User Reservation	Per 40G, 10G or 10/1G port			
Module Weight	Q3 and S12 versions—3.219 kg. Q2 and S8 versions—3.145 kg. Q1 and S4 versions—3.066 kg			
Module Predicted MTBF	Q3 versions = 49,523 hours. Q2 versions = 56,330 hours. Hours of continuous operation			
Operating Temperature Range	All mX2 modules are supported for 59° to 95° F (15° to 35° C) ambient temperature. 20% to 80% relative humidity.			
Max Power Draw Per Module	mX2-40G-Q3 draws a maximum of 420W per slot.			

### Spirent TestCenter Layer 1 Testing—Supported for 40GbE Modules Only

PCS Layer Testing	PCS skew injection and measurement for each lane. PCS lane swapping and swap detection. Sync header and alignment marker lock status per lane. Alignment errors, coding errors per PCS. PCS sync header errors, BIP8 errors, sync errors, length errors, consecutive errors, marker errors per PCS lane. Error counts include instantaneous LED indicator, count, number in last second, number of erred seconds and the error rate per second.
PMA Layer Testing	PRBS pattern generation per PMA lane. Pattern detection, errors, error rate per lane

## Technical Specifications

### Spirent TestCenter Layer 2-3 Traffic Generation

Transmit Streams Per Port (arbitrary values)	64K
Stream Block Definitions Per Port	512 stream block definitions each capable of generating multiple streams
Frame Templates Per Port	256 unique frame templates can be transmitted from each port
Transmit Statistics Per Port	Nearly 50 transmit stats per port reported in real time. Stats include L1, L2 and L3+ counters and rates and include counts for frames generated with CRC errors and checksum errors.
Transmit Statistics Per Stream	Tx Frame count and rate—all Tx statistics accurate even with random frame sizes and rates.
Error and Fault Generation	Link Fault Signaling and streamblock FCS error and IP checksum errors
Variable Field Definition (VFD) per Port	256 VFD indices per port
VFDs per Stream	6 VFDs per stream
Route Insertion Table (RIT) Entries per port	8M 4-byte entries for dynamic label or random IP/MAC address assignments
RIT or List VFD Entries per Stream	8 RIT insertions or List VFD insertions per stream
Frame Length Range	100% line rate for frames of 58-16383 bytes. Sub-line rate for frames from 33-57 bytes.
Frame Length Controls	Fixed, increment, decrement, random, automatic based on user frame, IMIX w/ weighting for 4 nodes
Frame Rate Minimum and Maximum at Wire Rate	1 every 3.43s to 102% of line rate
Scheduler Mode Support	<ul style="list-style-type: none"> <li>▪ Port Based—Traffic scheduling handled at the port level</li> <li>▪ Rate Based—Key parameters determined at the port level with division among the individual stream blocks</li> <li>▪ Priority Based—Scheduling determined at the stream block level using user-assigned priorities. Precise scheduling of CBR and bursty traffic for QoS testing</li> <li>▪ Manual Mode—Manual control of stream sequence</li> </ul>
Priority Flow Control	Generator supports up to 8 queues for responding to PFC Pause frames. Queue support can be integrated with DCBX emulation for automatic setup. PFC Pause frames can be sent manually for DUT response testing or triggered automatically based on configurable received traffic behavior.

### Spirent TestCenter Layer 2-3 Traffic Analysis

Trackable Streams Per Port	128K
Statistics Per Stream	<p>Over 40 real-time measurements per stream – includes standard frame and packet counters and rates and advanced sequence checking, RFC 4689 jitter, latency, FCS errors and checksum errors.</p> <ul style="list-style-type: none"> <li>▪ Advanced sequencing—In-order, lost, reordered, late and duplicate</li> <li>▪ Latency—Avg, min, max and short-term avg; first/last frame arrival timestamp</li> <li>▪ Data integrity—IP checksum, TCP/UDP checksum, frame CRC, embedded CRC and PRBS bit errors</li> </ul>
Statistics Per Port	<p>Over 50 transmit stats per port reported in real time. Stats include L1, L2 and L3+ counters and rates and include received FCS, checksum, and PRBS errors and rates. Also available are per-priority level PFC counters and six user-defined (pattern match) counters.</p> <p>Protocol port counters available for tracking key protocol message and state information for Routing and MPLS, Carrier Ethernet, GRE, ARP and PFC control plane.</p>
User-defined Statistics Per Port	Six user-defined statistics (count and rate for each) specified by regular expression (using AND, OR and NOT) consisting of byte pattern and offset match and/or frame length range match.

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## Technical Specifications

### Spirent TestCenter Layer 2-3 Traffic Analysis (continued)

<b>Analyzer Real-Time Filtering—Identify, display and filter by user-configurable protocol field values and ranges.</b>	Four 16-bit and one 32-bit analyzer filters available per-port for real-time stream analysis of test signature and non-test signature traffic.  Filters can be placed over protocol fields with masks and ranges to isolate specific types of traffic and by quality of service values such as: transmit stream ID, IPv4/v6 SA/DA, MAC SA/DA, IP TOS/DiffServ, TCP/UDP port, VLAN ID, VLAN priority, MPLS label, MPLS exp plus more
<b>Capture Buffer Size</b>	256 MB per port
<b>Capture Buffer Controls – Spirent TestCenter’s unique capture capability allows maximum effectiveness when debugging hard to find hardware or protocol problems.</b>	Several modes of operation that include: Filter by protocol fields, filter by byte offset and range; store slices or full-frames; store signature or all frames; store tx/rx control plane with data plane; real-time mode for control plane traffic; wrap or stop buffer at end.  User-defined pattern definitions can logically combine 8 filters of up to 32 total bytes. Patterns can be applied to start, filter (quality) or stop capture.  In addition to user-patterns, filtering, starting and stopping capture contains the following pre-defined events: FCS, PRBS, IPv4 checksum, TCP/UDP/IGMP checksum, and sequence errors; undersize, oversize, jumbo, and user-defined frame length; IPv4, IPv6, TCP, UDP and IGMP packets; test signature present and test stream ID match. Each event can be independently set to ignore, include or exclude.
<b>Priority Flow Control</b>	Per-priority measurements for Xon response time, PFC transmit time and post-PFC receive time.
<b>Latency Modes</b>	Benchmark tests support LIFO, LILO, FIFO or FILO latency calculation methods.
<b>High-resolution sampling – high-resolution sampling and charting available for select port or stream-block counters. Allows detailed analysis of events happening at the millisecond level (e.g., fail-over and re-route performance analysis)</b>	<ul style="list-style-type: none"><li>▪ Available on any receive port or streamblock frame/bit/byte counter or rate</li><li>▪ 1000 samples available at intervals of 1-100ms</li><li>▪ Sample trigger set by relational operator of user-defined value of sampled statistic</li><li>▪ User-defined trigger location within buffer</li></ul>
<b>Histograms</b>	Port histograms

### Spirent TestCenter Protocol Emulation

Spirent TestCenter protocols available as separately licensed packages. Below is a sample list of supported protocols. Contact Spirent for a full list of capabilities and packages.

<b>Enterprise and data center switch protocol support</b>	<ul style="list-style-type: none"><li>▪ OpenFlow 1.3 / 1.0—OpenFlow switch (planned for 2015) and controller emulation and switch conformance testing</li><li>▪ Routing, multicast and bridging—All major IPv4 and IPv6 unicast and multicast routing protocols, IGMPv1/v2/v3, MLDv1/v2, LACP, STP, RSTP and MSTP</li><li>▪ Data center—DCBX, FCoE, FIP, 802.1Qbb</li><li>▪ Stateful L4-7—HTTP, SIP and FTP</li></ul>
<b>Service Provider Protocol Support</b>	<ul style="list-style-type: none"><li>▪ SDN/NFV—PCE and Segment Routing</li><li>▪ Routing and MPLS— All major IPv4 and IPv6 unicast and multicast routing protocols, RSVP-TE, LDP, VPLS-LDP, VPLS-BGP, BGP/MPLS-VPN, Fast Re-route, EVPN, mVPN, P2MP-TE, BFD, TWAMP and PWE3 (RFC4447)</li><li>▪ Access—ANCP, PPPoE, DHCP, L2TP, IGMPv1/v2/v3, MLDv1/v2, DHCPv6 and PPPoEv6</li><li>▪ Carrier Ethernet and bridging: LACP, STP, RSTP and MSTP, 802.1ag CFM, Y.1731, PBB, PBB-TE, Link OAM</li><li>▪ Stateful L4-7—HTTP, SIP and FTP, Unicast/Multicast RTSP and RAW TCP</li><li>▪ Mobile Backhaul—MPLS-TP, 1588v2 and Synchronous Ethernet as supported protocols</li></ul>

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## Requirements

- Spirent Chassis and Controller (see table)
- Windows-based workstation with 10/100/1000 Mbps Ethernet NIC; mouse and color monitor required for GUI operation. For complete GUI requirements, please refer to Spirent TestCenter Generator and Analyzer Base Package A data sheet (P/N 79-000028)
- Linux or Windows-based workstation for command line automation. For complete automation system requirements, refer to the Spirent TestCenter Extreme Automation Package data sheet
- Spirent TestCenter hardware requires BPK-1001A for packet generation and analysis

## Spirent services

Spirent Global Services provides a variety of professional services, support services and education services—all focused on helping customers meet their complex testing and service assurance requirements. For more information, visit the [Global Services website at www.spirent.com](http://www.spirent.com) or contact your Spirent sales representative.

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### Ordering information

Part number	Description	Spirent Application		
		Spirent TestCenter	Avalanche Commander	Landslide
MX2-40G-Q3	Spirent mX2 40/10 GbE QSFP+ 3-ports	X		
MX2-40G-Q2	Spirent mX2 40/10 GbE QSFP+ 2-ports	X		
MX2-40G-Q1	Spirent mX2 40/10 GbE QSFP+ 1-ports	X		
MX2-40GO-Q3	Spirent mX2 40 GbE only QSFP+ 12-ports	X		
MX2-40GO-Q2	Spirent mX2 40 GbE only QSFP+ 8-ports	X		
MX2-40GO-Q1	Spirent mX2 40 GbE only QSFP+ 4-ports	X		
MX2-10G-Q3	Spirent mX2 10 GbE QSFP+ 12-ports	X		
MX2-10G-Q2	Spirent mX2 10 GbE QSFP+ 8-ports	X		
MX2-10G-Q1	Spirent mX2 10 GbE QSFP+ 4-ports	X		
MX2-10G-S12	Spirent mX2 10/1 GbE SFP+ 12-ports	X		
MX2-10G-S8	Spirent mX2 10/1 GbE SFP+ 8-ports	X		
MX2-10G-S4	Spirent mX2 10/1 GbE SFP+ 4-ports	X		

Contact Spirent for the latest information on supported Layer 4-7 applications

### Accessories for QSFP+ Interfaces

ACC-6076A	Optical transceiver, QSFP+, 40GBASE-SR4, 850NM, MPO, MMF
ACC-6089A	Optical transceiver, QSFP+ dual-rate, 40GBASE-SR4 / 4x10GBASE-SR, 850NM, MMF
ACC-6077A	Optical transceiver, QSFP+, 40GBASE-LR4, 1310NM, SMF
ACC-6090A	Optical transceiver, QSFP+ TO 4x10GBASE-LR, SMF
ACC-6085A	Copper direct-attach cable, QSFP+ TO QSFP+, 3-meter
ACC-6087A	Copper breakout cable assembly, QSFP+ TO 4 X SFP+, 3-meter

### Accessories for SFP+ interfaces

ACC-6081A	Optical transceiver, SFP+ dual-rate, 10 G-1 G, 850NM, MMF
ACC-6092A	Copper transceiver, SFP, 1000BASE-T RJ-24
ACC-6082A	Optical transceiver SFP+ dual-rate, 10 G-1 G, 1310NM, SMF
ACC-6050A	Optical transceiver SFP+ MSA, 10 GBE, 10GBASE-SR, MMF
ACC-6051A	Optical transceiver SFP+ MSA, 10 GBE, 10GBASE-LR, SMF
ACC-6060A	SFP+ passive copper cable assembly, 1-meter
ACC-6061A	SFP+ passive copper cable assembly, 3-meter

### Spirent chassis

SPT-N11U-110	Spirent N11U chassis and controller with 110 V AC power supplies
SPT-N11U-220	Spirent N11U chassis and controller with 220 V AC power supplies
SPT-N4U-110	Spirent N4U chassis and controller with 110 V AC power supplies
SPT-N4U-220	Spirent N4U chassis and controller with 220 V AC power supplies